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| **IEE JNL** | IEE Journal or Magazine |
| **IEEE CNF** | IEEE Conference Proceeding |
| **IEE CNF** | IEE Conference Proceeding |
| **IEEE STD** | IEEE Standard |

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| IEEE CNF | IEEE Conference Proceeding |
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**1**  Fast detection of communication patterns in distributed executions
   Thomas Kunz, Michiel F. H. Seuren
   November 1997 **Proceedings of the 1997 conference of the Centre for A on Collaborative research**
   **Publisher:** IBM Press
   Full text available: ⅌ pdf(4.21 MB)     Additional Information: full citation, abstr index terms

   Understanding distributed applications is a tedious and difficult task. Vis on process-time diagrams are often used to obtain a better understanding of the application. The visualization tool we use is Poet, an event tracer ( University of Waterloo. However, these diagrams are often very comple: provide the user with the desired overview of the application. In our expi display repeated occurrences of non-trivial commun ...

**2**  Static correlated branch prediction
⬥ Cliff Young, Michael D. Smith
   September 1999 **ACM Transactions on Programming Languages and S (TOPLAS),** Volume 21 Issue 5
   **Publisher:** ACM Press
   Full text available: ⅌ pdf     Additional Information: full citation, abstr

(508.49 KB) <u>citings</u>, <u>index tern</u>

Recent work in history-based branch prediction uses novel hardware strt branch correlation and increase branch prediction accuracy. Branch corre when the outcome of a conditional branch can be accurately predicted by outcomes of previously executed branches in the dynamic instruction str article, we show how to instrument a program so that it is practical to co statistics that indicate where branch correl ...

**Keywords**: branch correlation, branch prediction, path profiling, profile-optimization

**3** <u>Informing memory operations: memory performance feedback mechanism; applications</u>

Mark Horowitz, Margaret Martonosi, Todd C. Mowry, Michael D. Smith
May 1998 **ACM Transactions on Computer Systems (TOCS)**, Volume
**Publisher:** ACM Press

Full text available: pdf          Additional Information: <u>full citation</u>, <u>abstr</u>
          (344.74 KB)                                           <u>citings</u>, <u>index tern</u>

Memory latency is an important bottleneck in system performance that c adequately solved by hardware alone. Several promising software techni shown to address this problem successfully in specific situations. Howev of these software approaches has been limited because current architectu provide a fine-grained, low-overhead mechanism for observing and reaci behavior directly. To fill this need, this article proposes a new class ...

**Keywords**: cache miss notification, memory latency, processor architect

**4** <u>An intermediate representation for behavioral synthesis</u>

Nikil D. Dutt, Tedd Hadley, Daniel D. Gajski
January 1991 **Proceedings of the 27th ACM/IEEE conference on Desigr**
**Publisher:** ACM Press

Full text available: pdf          Additional Information: <u>full citation</u>, <u>abstr</u>
          (728.22 KB)                                           <u>citings</u>, <u>index tern</u>

This paper describes an intermediate representation for behavioral and st

that is based on annotated state tables. It facilitates user control of the sy:
allowing specification of partially design structures, and a mixture of bel
and user specified bindings between the abstract behavior and the structu
general model allows the capture of synchronous and asynchronous beha
hierarchical descriptions with concurre ...

**5** Informing memory operations: providing memory performance feedback ii
processors
Mark Horowitz, Margaret Martonosi, Todd C. Mowry, Michael D. Smith
May 1996 **ACM SIGARCH Computer Architecture News , Proceeding**
**annual international symposium on Computer architecture**:
Volume 24 Issue 2
**Publisher:** ACM Press
Full text available: pdf(1.55    Additional Information: full citation, abstr
MB)                        citings, index tern

Memory latency is an important bottleneck in system performance that c
adequately solved by hardware alone. Several promising software techni
shown to address this problem successfully in specific situations. Howev
of these software approaches has been limited because current architectu
a fine-grained, low-overhead mechanism for observing and reacting to m
directly. To fill this need, we propose a new class of memory operati ...

**6** Slicing real-time programs for enhanced schedulability
Richard Gerber, Seongsoo Hong
May 1997 **ACM Transactions on Programming Languages and System**
Volume 19 Issue 3
**Publisher:** ACM Press
Full text available: pdf      Additional Information: full citation, abstr
(378.88 KB)                  citings, index tern

In this article we present a compiler-based technique to help develop cor
systems. The domain we consider is that of multiprogrammed real-time ;
which periodic tasks control physical systems via interacting with extern
actuators. While a system is up and running, these operations must be pe
specified—otherwise the system may fail. Correctness depends not only
individually, but also on the time-multiplexed behavior of ...

**Keywords**: priority assignment, program slicing, static priority schedulii

**7** Monitoring semantics: a formal framework for specifying, implementing, a
 about execution monitors
Amir Kishon, Paul Hudak, Charles Consel
May 1991 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLA**
　　　　 **conference on Programming language design and implemen**
　　　　 Volume 26 Issue 6
**Publisher:** ACM Press
Full text available: pdf(1.19　Additional Information: full citation, refer
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**8** TAOS: Testing with Analysis and Oracle Support
 Debra J. Richardson
August 1994 **Proceedings of the 1994 ACM SIGSOFT international syn**
　　　　 **Software testing and analysis**
**Publisher:** ACM Press
Full text available: pdf(1.49　Additional Information: full citation, abstr
　　　　　　 MB)　　　　　　　　　　　　　 citings, index tern

　　 Few would question that software testing is a necessary activity for assui
　　 quality, yet the typical testing process is a human intensive activity and a
　　 unproductive, error-prone, and often inadequately done. Moreover, testir
　　 a prominent place in software development or maintenance processes, nc
　　 part of them. Major productivity and quality enhancements can be achiev
　　 the testing process through tool development and u ...

**9** STATEMATE applied to statistical software testing
 P. Thévenod-Fosse, H. Waeselynck
July 1993 **ACM SIGSOFT Software Engineering Notes , Proceedings o**
　　　　 **SIGSOFT international symposium on Software testing and**
　　　　 **'93,** Volume 18 Issue 3
**Publisher:** ACM Press
Full text available: pdf(1.31　Additional Information: full citation, abstr
　　　　　　 MB)　　　　　　　　　　　　　 citings, index tern
　　 This paper is concerned with the use of statistical testing as a verificatior

complex software. Statistical testing involves exercising a program with the test profile and the number of generated inputs being determined acc based on program structure or software functionality. In case of complex probabilistic generation must be based on a black box analysis, the adopt defined from behavior model ...

**10** <u>Using weaves for software construction and analysis</u>
Michael M. Gorlick, Rami R. Razouk
May 1991 **Proceedings of the 13th international conference on Softwar**
**Publisher:** IEEE Computer Society Press
Full text available: <u>pdf(1.30 MB)</u>    Additional Information: <u>full citation, refer</u>

**11** <u>Visual programming: the outlook from academia and industry</u>
K. N. Whitley, Alan F. Blackwell
October 1997 **Papers presented at the seventh workshop on Empirical**
            **programmers**
**Publisher:** ACM Press
Full text available: <u>pdf(2.46 MB)</u>   Additional Information: <u>full citation, refer</u> <u>index terms</u>

**12** <u>A safe, efficient regression test selection technique</u>
Gregg Rothermel, Mary Jean Harrold
April 1997 **ACM Transactions on Software Engineering and Methodol**
           Volume 6 Issue 2
**Publisher:** ACM Press
Full text available: <u>pdf (730.74 KB)</u>   Additional Information: <u>full citation, abstr</u> <u>citings, index tern</u>

Regression testing is an expensive but necessary maintenance activity pe modified software to provide confidence that changes are correct and do affect other portions of the softwore. A regression test selection techniqu existing test set, thests that are deemed necessary to validate modified so present a new technique for regression test selection. Our algorithms con flow graphs for a precedure or program and its modified ver ...

**Keywords**: regression test selection, regression testing, selective retest

**13** ATOM: a system for building customized program analysis tools
Amitabh Srivastava, Alan Eustace
June 1994 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLA
        conference on Programming language design and implemen**
        Volume 29 Issue 6
**Publisher:** ACM Press

Full text available: pdf      Additional Information: full citation, abstr
      (965.20 KB)                  citings, index tern

ATOM (Analysis Tools with OM) is a single framework for building a v
customized program analysis tools. It provides the common infrastructur
code-instrumenting tools; this is the difficult and time-consuming part. T
defines the tool-specific details in instrumentation and analysis routines.
block counting tool like Pixie with ATOM requires only a page of code.
OM link-time technology, organizes the final execu ...

**14** The Command and Control Communications and Information Network An
(C3INAT)
John P. Mullen, Jason W. Rupe, Srinagesh Gavirneni, Way Kuo
December 1990 **Proceedings of the 22nd conference on Winter simulati**
**Publisher:** IEEE Press

Full text available: pdf      Additional Information: full citation, refer
      (719.17 KB)                  terms

**15** Revised5 report on the algorithmic language scheme
N. I. Adams, D. H. Bartley, G. Brooks, R. K. Dybvig, D. P. Friedman, R. F
Hanson, C. T. Haynes, E. Kohlbecker, D. Oxley, K. M. Pitman, G. J. Roza
J. Sussman, M. Wand, H. Abelson
September 1998 **ACM SIGPLAN Notices**, Volume 33 Issue 9
**Publisher:** ACM Press

Full text available: pdf(4.44
      MB)      Additional Information: full citation, citin

**16** <u>OCM—a monitoring system for interoperable tools</u>
Roland Wismüller, Jög Trinitis, Thomas Ludwig
August 1998 **Proceedings of the SIGMETRICS symposium on Parallel tools**
**Publisher:** ACM Press
Full text available: <u>pdf(1.31 MB)</u>     Additional Information: <u>full citation, refer index terms</u>

**17** <u>A BIST scheme for RTL controller-data paths based on symbolic testabilit</u>
Indradeep Ghosh, Niraj K. Jha, Sudipta Bhawmik
May 1998 **Proceedings of the 35th annual conference on Design autom:**
**Publisher:** ACM Press
Full text available: <u>pdf (409.71 KB)</u> Additional Information: <u>full citation, abstr</u>
<u>Publisher Site</u>                                    <u>citings, index tern</u>

This paper introduces a novel scheme for testing register-transfer level c
paths using built-in self-test (BIST). The scheme uses the controller netli
path of a circuit to extract a test control/data flow (TCDF) which consist:
mapped to modules in the circuit and variables mapped to registers. This
derive a set of symbolic justification and propagation paths (known as te
test some of the operations and vari ...

**18** <u>Lexical rules in constraint-based grammars</u>
Ted Briscoe, Ann Copestake
December 1999 **Computational Linguistics,** Volume 25 Issue 4
**Publisher:** MIT Press
Full text available: <u>pdf(2.82 MB)</u>
<u>Publisher Site</u>     Additional Information: <u>full citation, abstr citings</u>

Lexical rules have been used to cover a very diverse range of phenomen:
based grammars. Examination of the full range of rules proposed shows

(1991) postulated upper bound on the length of list-valued attributes sucl
the lexicon cannot be maintained, leading to unrestricted generative capa
based formalisms utilizing HPSG-style lexical rules. We argue that it is ]
subdivide such rules into a class of semiproductive lexicall ...

**19** Efficient and flexible fault tolerance and migration of scientific simulation:
CUMULVS
James Arthur Kohl, Philip M. Papadopoulas
August 1998 **Proceedings of the SIGMETRICS symposium on Parallel
tools**
**Publisher:** ACM Press
Full text available: pdf(1.76    Additional Information: full citation, refer
MB)                                                     index terms

**20** Version models for software configuration management
Reidar Conradi, Bernhard Westfechtel
June 1998 **ACM Computing Surveys (CSUR)**, Volume 30 Issue 2
**Publisher:** ACM Press
Full text available: pdf         Additional Information: full citation, abstr
(483.54 KB)                                       citings, index tern

After more than 20 years of research and practice in software configurati
(SCM), constructing consistent configurations of versioned software pro
a challenge. This article focuses on the version models underlying both c
systems and research prototypes. It provides an overview and classificati
versioning paradigms and defines and relates fundamental concepts such
variants, configurations, and changes. In particular, we foc ...

**Keywords**: changes, configuration rules, configurations, revisions, varia

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**1  Level set and PDE methods for computer graphics**
◈ David Breen, Ron Fedkiw, Ken Museth, Stanley Osher, Guillermo Sapiro,
   August 2004 **Proceedings of the conference on SIGGRAPH 2004 cours**
                  **'04**
   **Publisher:** ACM Press
   Full text available: ▣ pdf(17.07 MB)   Additional Information: full citation, abstr

   Level set methods, an important class of partial differential equation (PD
   define dynamic surfaces implicitly as the level set (iso-surface) of a sam]
   function. The course begins with preparatory material that introduces the
   partial differential equations to solve problems in computer graphics, ge(
   and computer vision. This will include the structure and behavior of sev(
   of differential equations, e.g. the level set eq ...

**2  Status report of the graphic standards planning committee of ACM/SIGGR**
◈ the-art of graphic software packages
   Compuater Graphics staff
   September 1977 **ACM SIGGRAPH Computer Graphics**, Volume 11 Is:
   **Publisher:** ACM Press
   Full text available: ▣ pdf(9.03 MB)   Additional Information: full citation, refer

**3** Fast detection of communication patterns in distributed executions

Thomas Kunz, Michiel F. H. Seuren

November 1997 **Proceedings of the 1997 conference of the Centre for A on Collaborative research**

**Publisher:** IBM Press

Full text available: pdf(4.21 MB)     Additional Information: full citation, abstr index terms

Understanding distributed applications is a tedious and difficult task. Vis on process-time diagrams are often used to obtain a better understanding of the application. The visualization tool we use is Poet, an event tracer ( University of Waterloo. However, these diagrams are often very comple: provide the user with the desired overview of the application. In our exp display repeated occurrences of non-trivial commun ...

**4** Status report of the graphic standards planning committee

Computer Graphics staff

August 1979 **ACM SIGGRAPH Computer Graphics**, Volume 13 Issue

**Publisher:** ACM Press

Full text available: pdf(15.01 MB)     Additional Information: full citation, refer

**5** Crowd and group animation

Daniel Thalmann, Christophe Hery, Seth Lippman, Hiromi Ono, Stephen F Sutton

August 2004 **Proceedings of the conference on SIGGRAPH 2004 cours '04**

**Publisher:** ACM Press

Full text available: pdf(20.19 MB)     Additional Information: full citation, abstr

A continuous challenge for special effects in movies is the production of crowds, in terms of rendering and behavior. This course will present stat techniques and methods. The course will explain in details the different ( create virtual crowds: particle systems with flocking techniques using att repulsion forces, copy and pasting techniques, agent-based methods. The

software tools will be presented including the MASSIVE softwa ...

**6** GPGPU: general purpose computation on graphics hardware
David Luebke, Mark Harris, Jens Krüger, Tim Purcell, Naga Govindaraju, Woolley, Aaron Lefohn
August 2004 **Proceedings of the conference on SIGGRAPH 2004 cours**
**'04**
**Publisher:** ACM Press
Full text available: pdf(63.03 MB)     Additional Information: full citation, abstr

The graphics processor (GPU) on today's commodity video cards has ev extremely powerful and flexible processor. The latest graphics architectu tremendous memory bandwidth and computational horsepower, with ful vertex and pixel processing units that support vector operations up to ful point precision. High level languages have emerged for graphics hardwa computational power accessible. Architecturally, GPUs are highly parall

**7** Special section: Reasoning about structure, behavior and function
B. Chandrasekaran, Rob Milne
July 1985 **ACM SIGART Bulletin**, Issue 93
**Publisher:** ACM Press
Full text available: pdf(5.13 MB)     Additional Information: full citation, abstr

The last several years' of work in the area of knowledge-based systems h deeper understanding of the potentials of the current generation of ideas, importantly, also about their limitations and the need for research both ir framework as well as in new directions. The following ideas seem to us t note in this connection.

**8** Informing memory operations: memory performance feedback mechanism applications
Mark Horowitz, Margaret Martonosi, Todd C. Mowry, Michael D. Smith
May 1998 **ACM Transactions on Computer Systems (TOCS)**, Volume
**Publisher:** ACM Press
Full text available: pdf (344.74 KB)     Additional Information: full citation, abstr
citings, index tern

Memory latency is an important bottleneck in system performance that c
adequately solved by hardware alone. Several promising software techni
shown to address this problem successfully in specific situations. Howev
of these software approaches has been limited because current architectu
provide a fine-grained, low-overhead mechanism for observing and react
behavior directly. To fill this need, this article proposes a new class ...

**Keywords**: cache miss notification, memory latency, processor architect

**9** A VHDL-AMS compiler and architecture generator for behavioral synthes
systems
Alex Doboli, Ranga Vemuri
January 1999 **Proceedings of the conference on Design, automation and**
**Publisher:** ACM Press
Full text available: pdf
　　　　　　　　　(104.79 KB)  Additional Information: full citation, citin

**10** Modeling statecharts and activitycharts as signal equations
J.-R. Beauvais, E. Rutten, T. Gautier, R. Houdebine, P. Le. Guernic, Y.-M.
October 2001 **ACM Transactions on Software Engineering and Methoc**
　　　　　　**(TOSEM),** Volume 10 Issue 4
**Publisher:** ACM Press
Full text available: pdf          Additional Information: full citation, abstr
　　　　　　(299.20 KB)                                   index terms, revie

The languages for modeling reactive systems are of different styles, like
state-based ones and the declarative, data-flow ones. They are adapted tc
application domains. This paper, through the example of the languages S
Signal, shows a way to give a model of an imperative specification (Stat
declarative, equational one (Signal). This model constitutes a formal mo
Statemate semantics of Statecharts, upon which formal analysis tec ...

**Keywords**: behavioral modeling, ctatemate, reactive systems, signal, syı
languages, tatecharts

**11** <u>The KScalar simulator</u>

J. C. Moure, Dolores I. Rexachs, Emilio Luque

March 2002 **Journal on Educational Resources in Computing (JERIC)**,
1

**Publisher:** ACM Press

Full text available: pdf         Additional Information: <u>full citation</u>, <u>abstr</u>
(493.35 KB)                                            <u>index terms</u>

Modern processors increase their performance with complex microarchit
mechanisms, which makes them more and more difficult to understand a
KScalar is a graphical simulation tool that facilitates the study of such pi
allows students to analyze the performance behavior of a wide range of i
microarchitectures: from a very simple in-order, scalar pipeline, to a deta
superscalar pipeline with non-blocking caches, speculative execution, an

**Keywords**: Education, pipelined processor simulator

**12** <u>Special issue on knowledge representation</u>

Ronald J. Brachman, Brian C. Smith

February 1980 **ACM SIGART Bulletin**, Issue 70
**Publisher:** ACM Press

Full text available: pdf(13.13
MB)        Additional Information: <u>full citation</u>, <u>abstr</u>

In the fall of 1978 we decided to produce a special issue of the SIGART
devoted to a survey of current knowledge representation research. We fe
twe useful functions such an issue could serve. First, we hoped to elicit a
how people working in this subdiscipline understand knowledge represei
to illuminate the issues on which current research is focused, and to cata
approaches and techniques are currently being developed. Secon ...

**13** <u>Recognizing communication patterns: A probabilistic inference of multipai</u>
<u>structure based on Markov-switching models of gaze patterns, head directio</u>
<u>utterances</u>

Kazuhiro Otsuka, Yoshinao Takemae, Junji Yamato

October 2005 **Proceedings of the 7th international conference on Multii**
**ICMI '05**
**Publisher:** ACM Press

Full text available: 📄 pdf        Additional Information: <u>full citation</u>, <u>abstr</u>
                 <u>(513.38 KB)</u>                                          <u>index terms</u>

A novel probabilistic framework is proposed for inferring the structure o
face-to-face multiparty communication, based on gaze patterns, head dir
presence/absence of utterances. As the structure of conversation, this stu
combination of participants and their participation roles. First, we assess
that frequently appear in conversations, and define typical types of conv
called conversational regime, and hypothesi ...

**Keywords**: Gibbs sampler, Markov chain Monte Carlo, Markov-switchi
dynamic Bayesian network, eye gaze, face-to-face multiparty conversati
cues

**14** <u>Pushdown automata for user interface management</u>
◈ Dan R. Olsen
July 1984 **ACM Transactions on Graphics (TOG)**, Volume 3 Issue 3
**Publisher:** ACM Press
Full text available: 📄 <u>pdf(1.44</u>    Additional Information: <u>full citation</u>, <u>refer</u>
                 <u>MB)</u>                                          <u>index terms</u>, <u>revie</u>

**15** <u>Bug isolation via remote program sampling</u>
◈ Ben Liblit, Alex Aiken, Alice X. Zheng, Michael I. Jordan
May 2003 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLA
       conference on Programming language design and implemen**
       Volume 38 Issue 5
**Publisher:** ACM Press
Full text available: 📄 <u>pdf</u>        Additional Information: <u>full citation</u>, <u>abstr</u>
                 <u>(258.37 KB)</u>                                          <u>citings</u>, <u>index tern</u>

We propose a low-overhead sampling infrastructure for gathering inform
executions experienced by a program's user community. Several exampl
illustrate ways to use sampled instrumentation to isolate bugs. Assertion
be transformed to share the cost of assertions among many users. Lackin
broad guesses can be made about predicates that predict program errors ;
elimination used to whittle these down to the true bug. Finally, even ...

**Keywords**: assertions, bug isolation, feature selection, logistic regressioi sampling, statistical debugging

**16** <u>Using the Alfa-1 simulated processor for educational purposes</u>
Gabriel A. Wainer, Sergio Daicz, Luis F. De Simoni, Demian Wassermann
December 2001 **Journal on Educational Resources in Computing (JER** Issue 4
**Publisher:** ACM Press
Full text available: pdf              Additional Information: <u>full citation</u>, <u>abstr</u>
(238.65 KB)                                        <u>index terms</u>

Alfa-1 is a simulated computer designed for computer organization cour: accompanying toolkit allow students to acquire practical insights into de hardware by extending existing components. The DEVS formalism is us individual components and to integrate them into a hierarchy that descril behavior of different levels of a computer's architecture. We introduce A toolkit, show how to extend existing components, and describe how ...

**Keywords**: DEVS formalism, modeling computer architectures, systems

**17** <u>An intermediate representation for behavioral synthesis</u>
Nikil D. Dutt, Tedd Hadley, Daniel D. Gajski
January 1991 **Proceedings of the 27th ACM/IEEE conference on Desigr**
**Publisher:** ACM Press
Full text available: pdf              Additional Information: <u>full citation</u>, <u>abstr</u>
(728.22 KB)                                        <u>citings</u>, <u>index tern</u>

This paper describes an intermediate representation for behavioral and st that is based on annotated state tables. It facilitates user control of the sy: allowing specification of partially design structures, and a mixture of bel and user specified bindings between the abstract behavior and the structt general model allows the capture of synchronous and asynchronous beha hierarchical descriptions with concurre ...

**18**
<u>Special issue: AI in engineering</u>

D. Sriram, R. Joobbani
April 1985 **ACM SIGART Bulletin**, Issue 92
**Publisher:** ACM Press
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The papers in this special issue were compiled from responses to the ann
July 1984 issue of the SIGART newsletter and notices posted over the A
interest being shown in this area is reflected in the sixty papers received
countries. About half the papers were received over the computer netwo

**19** Human-computer interface development: concepts and systems for its man
H. Rex Hartson, Deborah Hix
March 1989 **ACM Computing Surveys (CSUR)**, Volume 21 Issue 1
**Publisher:** ACM Press
Full text available: pdf(7.97 MB)        Additional Information: full citation, abstr
citings, index tern

*Human-computer interface management*, from a computer science view
the process of developing quality human-computer interfaces, including
representation, design, implementation, execution, evaluation, and maint
survey presents important concepts of interface management: dialogue in
structural modeling, representation, interactive tools, rapid prototyping,
methodologies, and control structures. *Dialogue independence* is th ...

**20** An entity-life modeling approach to the design of concurrent software
Bo Sanden
March 1989 **Communications of the ACM**, Volume 32 Issue 3
**Publisher:** ACM Press
Full text available: pdf(1.37 MB)        Additional Information: full citation, abstr
citings, index tern

Using the idea of Entity-Life Modeling for task decomposition, the desig
prototypical elevator control system is explored and compared to an earl

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| L2 | 143 | 717/130.ccls. and condition$4 | USPAT | OR | OFF | 2006/05/23 13:50 |
| L3 | 29 | 717/130.ccls. and condition$4 and (log$4 or record$3 or stor$3 ) near3 (status or state or behavior) | USPAT | OR | OFF | 2006/05/23 13:55 |
| L4 | 3 | 717/130.ccls. and condition$4 and (log$4 or record$3 or stor$3 ) near3 (status or state or behavior) and ((instrument$5 or add$3 or insert$3) near3 conditional$2 ) | USPAT | OR | OFF | 2006/05/23 13:59 |
| L5 | 2 | 714/35.ccls. and condition$4 and (log$4 or record$3 or stor$3 ) near3 (status or state or behavior) and ((instrument$5 or add$3 or insert$3) near3 conditional$2 ) | USPAT | OR | OFF | 2006/05/23 14:08 |
| L6 | 213 | 714/35.ccls. | USPAT | OR | OFF | 2006/05/23 14:08 |
| S1 | 3 | ((insert$3 near3 (handler or interrupt or event) ) near5 (sampl$3 or measur$4 ) ) and ((boolean or condition$4 or guard$3 or predicat$3 ) near3 (sampl$3 or measur$4) ) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/10/31 15:17 |
| S2 | 48 | ((insert$3 near3 (handler or interrupt or event) ) same (sampl$3 or measur$4 ) ) and ((boolean or condition$4 or guard$3 or predicat$3 ) near3 (sampl$3 or measur$4) ) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/10/31 15:30 |
| S3 | 22 | ((instrument$5 near3 (handler or interrupt or event) ) same (sampl$3 or measur$4 ) ) and ((boolean or condition$4 or guard$3 or predicat$3 ) near3 (sampl$3 or measur$4) ) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/10/31 15:30 |
| S4 | 57 | ("5151981").URPN. | USPAT | OR | OFF | 2005/10/31 15:56 |
| S5 | 12 | ("6237073").URPN. | USPAT | OR | OFF | 2005/10/31 16:45 |
| S6 | 10 | ("6237073").URPN. and (condition$3 or boolean or predicate ) | USPAT | OR | OFF | 2005/10/31 16:46 |
| S7 | 10 | ("6237073").URPN. and (condition$3 or boolean or predicate ) | USPAT | OR | OFF | 2005/10/31 16:56 |
| S8 | 381050 | (sampl$3 or profil$3 or monitor$3 or measur$4 ) same (condition$3 or boolean or predicate ) | USPAT | OR | OFF | 2005/10/31 16:57 |
| S9 | 23874 | (insert$3 or instrument$5 ) same (sampl$3 or profil$3 or monitor$3 or measur$4 ) same (condition$3 or boolean or predicate ) | USPAT | OR | OFF | 2005/10/31 16:57 |

# EAST Search History

| S10 | 4126 | (insert$3 or instrument$5 ) same (sampl$3 or profil$3 or monitor$3 or measur$4 ) near3 (condition$3 or boolean or predicate ) | USPAT | OR | OFF | 2005/10/31 16:58 |
|-----|------|-------------------------------------------------|-------|----|-----|------------------|
| S11 | 2988 | (insert$3 or instrument$5 ) same (sampl$3 or profil$3 or monitor$3 or measur$4 ) near2 (condition$3 or boolean or predicate ) | USPAT | OR | OFF | 2005/10/31 16:58 |
| S12 | 724 | (insert$3 or instrument$5 ) near5 ((sampl$3 or profil$3 or monitor$3 or measur$4 ) near2 (condition$3 or boolean or predicate ) ) | USPAT | OR | OFF | 2005/10/31 16:59 |
| S13 | 3 | (insert$3 or instrument$5 ) near5 ((sampl$3 or profil$3 or monitor$3 or measur$4 ) near2 (condition$3 or boolean or predicate ) ) and 717/???.ccls. | USPAT | OR | OFF | 2005/10/31 17:00 |
| S14 | 21 | (insert$3 or instrument$5 ) near5 execut$4 and ((sampl$3 or profil$3 or monitor$3 or measur$4 ) near2 (condition$3 or boolean or predicate ) ) and (handler or function or operation or call ) and 717/???.ccls. | USPAT | OR | OFF | 2005/10/31 17:01 |
| S15 | 14 | ("5960198").URPN. | USPAT | OR | OFF | 2005/11/01 07:13 |
| S16 | 4 | ("5710724" "6249912" "6327699" "5995754").pn. | USPAT | OR | OFF | 2005/11/01 07:38 |
| S17 | 1 | "5960198".pn. | USPAT | OR | OFF | 2005/11/01 07:38 |
| S18 | 14 | ("5960198").URPN. | USPAT | OR | OFF | 2005/11/01 07:39 |
| S19 | 0 | "6971091".pn. | USPAT | OR | OFF | 2005/11/14 15:56 |
| S20 | 0 | "adaptively optimizing program".ti. | USPAT | OR | OFF | 2005/11/14 15:57 |
| S21 | 1 | "international business machines".as. and arnold.in. and fink.in. | USPAT | OR | OFF | 2005/11/14 16:02 |
| S22 | 28 | "international business machines".as. and "yield point" | USPAT | OR | OFF | 2005/11/14 16:13 |
| S23 | 0 | "sampling at selected program points" | USPAT | OR | OFF | 2005/11/14 16:13 |
| S24 | 31 | "sampling" and "program points" | USPAT | OR | OFF | 2005/11/14 16:13 |
| S25 | 206 | 717/130.ccls. | USPAT | OR | OFF | 2005/11/22 12:15 |
| S26 | 50 | 717/133.ccls. | USPAT | OR | OFF | 2005/11/22 12:15 |
| S27 | 311 | 717/127.ccls. | USPAT | OR | OFF | 2005/11/22 12:15 |
| S28 | 200 | 714/35.ccls. | USPAT | OR | OFF | 2005/11/22 12:15 |
| S29 | 0 | 717/130.ccls. and ("yield point" or (safe$2 adj point)) and (condition$4 or uncondition$4) | USPAT | OR | OFF | 2005/11/22 12:19 |

# EAST Search History

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|---|---|---|---|---|---|---|
| S30 | 0 | 717/133.ccls. and ("yield point" or (safe$2 adj point)) and (condition$4 or uncondition$4) | USPAT | OR | OFF | 2005/11/22 12:19 |
| S31 | 0 | 717/127.ccls. and ("yield point" or (safe$2 adj point)) and (condition$4 or uncondition$4) | USPAT | OR | OFF | 2005/11/22 12:20 |
| S32 | 137 | 714/35.ccls. and (condition$4 or uncondition$4) | USPAT | OR | OFF | 2005/11/22 12:20 |
| S33 | 0 | 714/35.ccls. and ("yield point" or (safe$2 adj point)) and (condition$4 or uncondition$4) | USPAT | OR | OFF | 2005/11/22 12:20 |
| S34 | 182 | 717/127.ccls. and (condition$4 or uncondition$4) | USPAT | OR | OFF | 2005/11/22 12:20 |
| S35 | 30 | 717/133.ccls. and (condition$4 or uncondition$4) | USPAT | OR | OFF | 2005/11/22 12:21 |
| S36 | 130 | 717/130.ccls. and (condition$4 or uncondition$4) | USPAT | OR | OFF | 2005/11/22 12:30 |
| S37 | 0 | "5710724".pn. and condition$4 and uncondition$4 | USPAT | OR | OFF | 2005/11/22 12:31 |
| S38 | 1 | "5710724".pn. and ( condition$4 or uncondition$4 ) | USPAT | OR | OFF | 2005/11/22 12:31 |
| S39 | 23 | ("4231106" \| "4740895" \| "5313616" \| "5335344" \| "5386522" \| "5430878" \| "5481688" \| "5528753" \| "5535329" \| "5537541" \| "5539907" \| "5560013").PN. OR ("5710724").URPN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/11/22 12:31 |
| S40 | 14 | (("4231106" \| "4740895" \| "5313616" \| "5335344" \| "5386522" \| "5430878" \| "5481688" \| "5528753" \| "5535329" \| "5537541" \| "5539907" \| "5560013").PN. OR ("5710724").URPN. ) and (condition$4 or uncondition$4 ) | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/11/22 12:37 |
| S41 | 90 | (execut$3 or run$4) near3 ((condition$4 or uncondition$4) near3 instrument$5) | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/11/22 12:43 |
| S42 | 0 | "6971091".pn. | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/11/22 13:00 |
| S43 | 1 | "6857120".pn. | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/11/22 14:24 |
| S44 | 2 | "20020112232" or "6405364".pn. | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/11/22 14:25 |
| S45 | 3 | ("5799286" "6073107" "5799286" "6555365" ).pn. | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/11/22 14:28 |

# EAST Search History

| S46 | 3 | ("5799286" "6073107" "5799286" "6553365" ).pn. | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/11/22 14:29 |
|-----|-----|-----|-----|-----|-----|-----|
| S47 | 4 | ("5799286" "6088717" "6073107" "6553365" ).pn. | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/11/22 16:52 |
| S48 | 501 | 705/59.ccls. | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/11/22 16:52 |